

Electronic Acknowledgement Receipt

EFS ID:	1070740
Application Number:	10689348
Confirmation Number:	4206
Title of Invention:	Low K dielectric integrated circuit interconnect structure
First Named Inventor:	Stephan Grunow
Customer Number:	23494
Filer:	Peter K. McLarty/Ann Trent
Filer Authorized By:	Peter K. McLarty
Attorney Docket Number:	TI-36564
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Submitted with Payment	yes
Payment was successfully received in RAM	\$500
RAM confirmation Number	183
Deposit Account	200668
The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows:	
Charge any Additional Fees required under 37 C.F.R. Section 1.16 and 1.17	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Appeal Brief Filed	T36564AppealBrief.pdf	39794	no	10
Warnings:					
Information:					
2	Fee Worksheet (PTO-875)	fee-info.pdf	8144	no	2
Warnings:					
Information:					
Total Files Size (in bytes):					47938
<p>This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.</p> <p>New Applications Under 35 U.S.C. 111 If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.</p> <p>National Stage of an International Application under 35 U.S.C. 371 If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.</p>					